

ABSTRACT

A carrier of circuit boards includes a base frame having a plurality of first card guides disposed on its upper surface, the first card guides for securing respective first side edges of a corresponding plurality of circuit boards, a plurality of second card guides for securing respective second side edges of the plurality of circuit boards, and a plurality of adjustment members, each being for individually adjusting a distance between a respective pair of one first card guide and one second card guide. A method of preparing a carrying arrangement for a plurality of circuit boards includes providing a carrier for the plurality of circuit boards. Groups of circuit boards having different vertical on-edge dimensions may be interconnected and/or tested by being placed together and individually snugly held on a single carrier shelf.

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